L Number	Hits	Search Text	DB	Time stamp
1	428758	(validat\$3 near1 connection near2	USPAT;	2004/09/05 12:40
		multi-wired cable) and (first and second	US-PGPUB;	
		electrical component\$1)	EPO; JPO; DERWENT;	
1			IBM TDB	
2	6585863	PLC system or programmable logic	USPĀT;	2004/09/05 12:42
		controller	US-PGPUB;	
		·	EPO; JPO;	
			DERWENT;	
3	2967152	processing module or processor\$1	IBM_TDB USPAT;	2004/09/05 12:42
"	2307132	processing modure or processory:	US-PGPUB;	2004/03/03 12.42
			EPO; JPO;	
			DERWENT;	
1_			IBM_TDB	
6	6120922	I/O module\$1 or system bus	USPAT;	2004/09/05 12:44
			US-PGPUB; EPO; JPO;	•
			DERWENT;	
			IBM TDB	
7	152100	adapter\$1	USPĀT;	2004/09/05 12:44
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM TDB	
8	62090	(processing module or processor\$1) and	USPAT;	2004/09/05 12:45
		adapter\$1	US-PGPUB;	1001,03,00 1110
			EPO; JPO;	
			DERWENT;	
9	11776	(I/O module\$1 or system bus) and	IBM_TDB USPAT;	2004/09/05 12:45
"	11//6	((processing module or processor\$1) and	US-PGPUB;	2004/09/03 12:43
		adapter\$1)	EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
10	17445		USPAT;	2004/09/05 12:46
		((processing module or processor\$1) and adapter\$1)) and (PLC system or	US-PGPUB; EPO; JPO;	
		programmable logic controller)	DERWENT;	
			IBM_TDB	
.11	387	((validat\$3 near1 connection near2	USPAT;	2004/09/05 12:46
		multi-wired cable) and (first and second	US-PGPUB;	
		electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing	EPO; JPO; DERWENT;	
		module or processor\$1) and adapter\$1)) and	IBM TDB	
		(PLC system or programmable logic	-	
		controller))		0004/00/55 55 55
12	1893016	generat\$3 and (pre-specified voltage level	USPAT;	2004/09/05 12:47
		or predetermined voltage level)	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
13	253		USPAT;	2004/09/05 12:47
		multi-wired cable) and (first and second	US-PGPUB;	
		electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing	EPO; JPO; DERWENT;	
1		module or processor\$1) and adapter\$1)) and	IBM TDB	
		(PLC system or programmable logic		
1		controller))) and (generat\$3 and		
		(pre-specified voltage level or		
14	3943246	predetermined voltage level)) powered up	USPAT;	2004/09/05 12:47
**	3,43246	Powered up	US-PGPUB;	2004/09/03 12:4/
			EPO; JPO;	
			DERWENT;	
			IBM TDB	

15	222	<pre>((((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))) and (generat\$3 and (pre-specified voltage level or predetermined voltage level))) and (powered up)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:48
16	1181650	detect\$3 and (desired voltage level)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/05 12:49
17	151	<pre>((((((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))) and (generat\$3 and (pre-specified voltage level or predetermined voltage level))) and (powered up)) and (detect\$3 and (desired voltage level))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:49
18	4155179	error signal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/05 12:49
19	27	<pre>(((((((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))) and (generat\$3 and (pre-specified voltage level or predetermined voltage level))) and (powered up)) and (detect\$3 and (desired voltage level))) and (error signal\$1)</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:49

12	15550	· +==	USPAT;	2004/09/04 17:09
		processor\$1	US-PGPUB;	
			EPO; JPO;	
l			DERWENT;	
1			IBM TDB	l
13	3639944	interlock circuit	USPAT:	2004/09/04 17:09
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
14	12	(((((test\$3 and (pre-specified voltage	USPAT;	2004/09/04 17:09
		level with component\$1)) and (714/14 or	US-PGPUB;	2004/03/04 17:03
		714/22 or 714/30 or 714/36 or 714/48 or	EPO; JPO;	
		714/721 or 714/724 or 714/733 or 714/734	DERWENT;	
Ì		or 714/799)) and (built-in power supply))	IBM TDB	
	1	and (assert\$3 and (error signal\$1 near2	12017_100	
		component\$1 with detected pre-specified		
1		voltage level\$1))) and ((first and second		
		components) and (powered up with		
	1	pre-specified voltage level))) and		
		(multi-wired cable)) and (validation and		
		protection circuitry near2 processor\$1)		
15	419309		USPAT;	2004/09/04 17:09
13	419309	powered adapter	· ·	2004/09/04 17:09
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
17	4344699	orner signal indigates	IBM TDB	0004/00/04 17 10
1 '	4344099	error signal indicator	USPAT;	2004/09/04 17:10
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1.6	1.0		IBM_TDB	
16	12		USPAT;	2004/09/04 18:01
		level with component\$1)) and (714/14 or	US-PGPUB;	
		714/22 or 714/30 or 714/36 or 714/48 or	EPO; JPO;	
		714/721 or 714/724 or 714/733 or 714/734	DERWENT;	
		or 714/799)) and (built-in power supply))	IBM_TDB	
		and (assert\$3 and (error signal\$1 near2		
		component\$1 with detected pre-specified		
1		voltage level\$1))) and ((first and second		
1		components) and (powered up with	1	
		pre-specified voltage level))) and		
		(multi-wired cable)) and (validation and		
		protection circuitry near2 processor\$1))	[
	I.,,	and (powered adapter)		